



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
-----------------	-------------	----------------------	---------------------	------------------

10/762,467

01/23/2004

Makiko Mori

02910.000113.

5471

5514

7590

06/01/2010

FITZPATRICK CELLA HARPER & SCINTO

1290 Avenue of the Americas

NEW YORK, NY 10104-3800

EXAMINER

SITTA, GRANT

ART UNIT

PAPER NUMBER

2629

MAIL DATE

DELIVERY MODE

06/01/2010

PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No. 10/762,467	Applicant(s) MORI, MAKIKO	
	Examiner GRANT D. SITTA	Art Unit 2629	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 24 February 2010.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-9 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-9 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 07 September 2007 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 103

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

2. The factual inquiries set forth in *Graham v. John Deere Co.*, 383 U.S. 1, 148 USPQ 459 (1966), that are applied for establishing a background for determining obviousness under 35 U.S.C. 103(a) are summarized as follows:

1. Determining the scope and contents of the prior art.
2. Ascertaining the differences between the prior art and the claims at issue.
3. Resolving the level of ordinary skill in the pertinent art.
4. Considering objective evidence present in the application indicating obviousness or nonobviousness.

3. Claims 1-9 are rejected under 35 U.S.C. 103(a) as being unpatentable over Tsuzuki et al (6,388,716) in view of Yamaguchi et al (5,675,391) hereinafter, Yamaguchi further in view, of Nagakubo et al (5,757,343) hereinafter, Nagakubo.

4. In regards to claim 1, Tsuzuki teaches a base method comprising a video display apparatus comprising:

a display panel (figs. 1 and 2, (18 and 33) respectively);

a converting circuit for executing conversion for an input video

signal to output a converted video signal (figs. 1 and 2 (12));

Art Unit: 2629

a display brightness featured value detecting circuit for detecting a display brightness featured value indicating a brightness of a display screen (fig. 1 and 2 (21 and 32));

an adjustment circuit for adjusting the converted video signal on the basis of said display brightness featured value to output an adjusted video signal (fig. 1 and 2 (20) ; and

Tsuzuki fails to teach a superimposing circuit for superimposing a signal for displaying textual information or an icon on the adjusted video signal to output a superimposed video signal to the display panel.

However, Yamaguchi teaches a known device with a superimposing circuit for superimposing a signal for displaying textual information or an icon on the adjusted video signal to output a superimposed video signal to the display panel. (fig. 3 (30) superimposing circuit placed at the last stage before being sent to the display). Examiner notes the second video signal is for a television receiver which contains text and icon information related to channel and station information.

It would have been obvious to one of ordinary skill in the art would have recognized that applying the known device of a superimposing circuit and would have yielded the predictable result of applying addition information on the video signal of Tsuzuki, in order to easily convey information to the user, while still providing proper brightness characteristics relating to the image.

Therefore, Tsuzuki as modified by Yamaguchi teaches wherein said display brightness screen (fig. 1 and 2 (21 and 32) Tsuzuki) featured value

Art Unit: 2629

detecting circuit receives the superimposed video (fig. 3 (30) Yamaguchi) signal output from said superimposing circuit before the superimposed video signal is input to the display panel (fig. 2 (32) before sent to 33 Tsuzuki), and calculates the display brightness featured value from the received superimposed video signal (fig. 3 (30) and (28) Yamaguchi) in view of col. 3-4, lines 62-35 Tsuzuki), and

wherein an image is displayed on the basis of the superimposed video signal output from said superimposing circuit (fig. 3 (30 Yamaguchi col. 6, lines 62-67)).

Furthermore, Tsuzuki and Yamaguchi fail to expressly teach a converting circuit for executing **nonlinear conversion** for an input video signal to output a converted video signal.

However, Nagakubo teaches a converting circuit for executing nonlinear conversion for an input video signal to output a converted video signal (fig. 4 ((2) and 3)). Examiner notes gamma correction is a non-linear correction of a video signal and it is performed before that A/D.

It would have been obvious to one of ordinary skill in the art to modify the converting circuit of Tsuzuki and Yamguchi to include the use of a gamma correction circuit in order to ensure the image is display properly.

5. In regards to claim 2, Tsuzuki and Yamaguchi as modified by Nagakubo teaches a video display apparatus as defined in claim 1, wherein said adjustment circuit is an adjustment circuit for adjusting the converted video signal on the

Art Unit: 2629

basis of a plurality of display brightness featured values which are sequentially detected (col.,2, lines 1-30 correction of brightness levels, at lowest, intermediate and highest levels as image is displayed Tsuzuki).

6. In regards to claim 3. Tsuzuki and Yamaguchi as modified by Nagakubo teaches a video display apparatus as defined in claim 1 or 2, wherein said adjustment circuit is also an adjustment circuit for adjusting the converted video signal on the basis of a brightness control value relating to an adjustment of image quality (fig. 1 and 2 (20) controller col. 4, lines 19-35 Tsuzuki).

7. In regards to claim 4, Tsuzuki and Yamaguchi as modified by Nagakubo (Previously Presented) A video display apparatus as defined in claim 1, wherein said display brightness featured value is a sum or average value of display signals for a predetermined period (fig. 3 R, G, and B Tsuzuki). Examiner notes the brightness features values are based on each color which is summed at, or before the display, for detection purposes.

8. In regards to claim 5, Tsuzuki and Yamaguchi as modified by Nagakubo teaches (Previously Presented) a video display apparatus as defined in claim 1, wherein said display brightness featured value is the number of signals of the display signals for a predetermined period which have a greater value than a predetermined value. (fig. 3 R, G, and B Tsuzuki). Examiner notes the brightness features values are based on each color which is summed at, or before the

Art Unit: 2629

display, for detection purposes.

9. In regards to claim 6, Tsuzuki and Yamaguchi as modified by Nagakubo teaches (Previously Presented) a video display apparatus as defined in claim 1, wherein said display brightness featured value is a sum or average value of display signals for each color for a predetermined period. (fig. 3 R, G, and B Tsuzuki). Examiner notes the brightness features values are based on each color which is summed at, or before the display, for detection purposes.

10. In regards to claim 7, Tsuzuki and Yamaguchi as modified by Nagakubo teaches a video display apparatus as defined in claim 1, wherein said display brightness featured value is a sum or average value of brightness components of display signals for a predetermined period. (fig. 3 R, G, and B Tsuzuki). Examiner notes the brightness features values are based on each color which is summed at, or before the display, for detection purposes

11. In regards to claim 8, Tsuzuki and Yamaguchi as modified by Nagakubo teaches (Previously Presented) a video display apparatus as defined in claim 1, wherein said display brightness featured value is a statistical value of display signals in a specific area of one display screen (fig. 2 statistical value of the current detected at (32) Tsuzuki).

Art Unit: 2629

12. In regards to claim 9, Tsuzuki and Yamaguchi as modified by Nagakubo (Previously Presented) A video display apparatus as defined in claim 1, wherein pixels of said video display apparatus are constructed of display elements arranged in matrix (fig. 14 horizontal and vertical characteristics of the display Tsuzuki) .

Response to Arguments

13. Applicant's arguments with respect to claims 1-9 have been considered but are moot in view of the new ground(s) of rejection.

Conclusion

14. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will

Art Unit: 2629

the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to GRANT D. SITTA whose telephone number is (571)270-1542. The examiner can normally be reached on M-F 9-6.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Sumati Lefkowitz can be reached on 571-272-3638. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Sumati Lefkowitz/
Supervisory Patent Examiner, Art Unit 2629

/Grant D Sitta/
Examiner, Art Unit 2629

